

Minutnik elnk - Fehler #272

PIN Standby verification

05.10.2022 11:56 - Maximilian Seesslen

Status:	Abgewiesen	Beginn:	05.10.2022
Priorität:	Normal	Abgabedatum:	
Zugewiesen an:	Maximilian Seesslen	% erledigt:	0%
Kategorie:		Geschätzter Aufwand:	0.00 Stunde
Zielversion:	v0.2.x	Aufgewendete Zeit:	0.00 Stunde
CS Zielversion:			

Beschreibung

E.g. the NJRST is internally connected to an pullup when in standby.

When an LED is connected it can be seen.

If such an pin is e.g. connected to an wakeup-button, it would also draw current in standby.

So in standby all voltages have to be measured to find pins that must not be used in wrong modes.

- Does it draw current when pushed high externaly? Resistance to GND.
- Does it draw current when pulled low externaly? Resistance to VDD.

Bootloader conditions are not that important. It will likely do something on the pins.

Historie

#1 - 05.10.2022 12:04 - Maximilian Seesslen

- Beschreibung aktualisiert

#2 - 06.10.2022 10:20 - Maximilian Seesslen

Just disable debug in standby mode.

According to [reference manual](#)

all GPIOs are high impedance in standby mode except configured wakeup pins and alarms.

#3 - 06.10.2022 10:38 - Maximilian Seesslen

- Status wurde von Neu zu Abgewiesen geändert

Just disable debug in standby mode.